Graduate Program Learning Outcomes for Electrical and Computer Engineering

GSLG #1: *Understand, interpret, shape, and augment the knowledge base.*

PLO #1: Demonstrate facility with methods of advanced engineering analysis and design.

PLO #2: Solve complex electrical and computer engineering problems by using advanced engineering analysis and design methods.

Satisfied by: Many courses in our curriculum; thesis or dissertation. Representative student work could be exams, class projects, theses or dissertations.

GSLG #2: *Share disciplinary expertise openly, effectively, and accurately.*

PLO #3: Create professional-quality reports, engineering design projects, and/or presentations with significant technical content that address complex electrical and computer engineering systems.

Satisfied by: Classes with a design project that includes a report and/or presentation; thesis or dissertation. Representative student work would be class projects, theses or dissertations.

GSLG #3: *Demonstrate responsible and ethical practice.*

PLO #4: Apply engineering methodologies such as safety, reliability, compliance, efficiency, and privacy/security into the design of hardware devices and systems, as well as software code.

Satisfied by: Classes with significant design content that incorporate safety, reliability, compliance, efficiency, and privacy/security into coursework, research, and thesis. Representative student work could be exams and class projects. RCR Training.

Table 1: Mapping of PLOs to the ECE core graduate courses

	Course						
PLO	ECE 515	ECE 523	ECE 550	ECE 565	ECE 571	ECE 573	
1	X	X	X		X	X	
2	X	X	X		X	X	
3				X	X		
4			X	X	X	X	